

CSE3442 Embedded Systems I

Fall 2019

Instructor Information

Instructor:

Jason Losh, Ph.D.

Office Number:

ERB 649

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Faculty Profile:

<https://mentis.uta.edu/explore/profile/jason-losh>

Office Hours:

Monday 10-10:50am, Tuesday 5-5:20pm, Thursday 12-12:30pm., Thursday 5-5:20pm, and by appointment. Additional hours will be added as needed as the semester proceeds.

Graders:

Grader contact information will be sent to the course listserv after assignments are made.

Course Information

Section Information:

001, 002, 003

Time and Place of Class Meetings:

Lecture: TTh 3:30-4:50pm, PH 210 (Section 001)

Laboratory: T 9-11:50am, ERB 127 (Section 002), Th 9-11:50am, ERB 127 (Section 003)

Description of Course Content:

Design of microcontroller-based systems, including microprocessor programming, component and system architectures, memory interfacing, asynchronous and synchronous serial interfaces, timer-based peripherals, analog to digital (A/D) and digital to analog (D/A) converters, and typical applications.. Prerequisites: CSE 2100, CSE 2312, CSE 2440, and CSE 2441.

Student Learning Outcomes:

Upon successful completion of this course, students will have knowledge of:

- Proficiency in C programming
- Differences between Harvard and von Neuman architectures
- Differences of microcontroller and microprocessor architectures
- Comparison of standard microprocessor and digital signal processor memory bus and ALU architectures
- Study of microcontroller peripherals including timers, PWM, interrupts, GPIO ports, and ADCs
- Determining microcontroller memory, speed, and capabilities to solve a task
- Interfacing with SPI and serial buses

- Measurement and instrumentation applications
- Digital filtering applications
- Real-time control applications

Class Web Page:

Additional files will be provided as needed on the course web site at <http://omega.uta.edu/~jlosh/>.

Communication:

All class-wide communication by the instructor, including distribution of homework sets, will occur via the class listserv. Please sign up for the CSE3442-L listserv by sending an e-mail from your UTA e-mail account to listserv@listserv.uta.edu from your UTA e-mail account (no subject line needed) and the command SUBSCRIBE CSE3442-L as the message body. You will then receive an e-mail from the listserv server to which you must acknowledge to join the listserv with "OK" in an e-mail.

Canvas will only be used for Echo360 access.

Textbooks and Other Course Materials:

No textbook will be required for this course. Extensive references, datasheets, application notes, and class notes will be provided on the course web site at <http://omega.uta.edu/~jlosh/>.

All students are required to own a TM4C123GXL evaluation kit. The cost is \$12.99. Information on this board is available at <http://www.ti.com/tool/EK-TM4C123GXL>. You will also need a prototyping board with donut rings for soldering your project. These are available in the IEEE office in NH for \$2 or from other sources.

Major Assignments and Examinations:

Test 1: Tuesday, October 15
 Test 2: Tuesday, November 26
 Project: Tuesday, December 4

Grading Information

Grading:

- Grade scale: A (90-100), B (80-89), C (70-79), D (60-69), and F (0-59)
- Grade calculation: Test 1 (30%), Test 2 (30%), Project (30%), Labs (10%)
- The instructor reserves the right to make reasonable changes in performance evaluation as needed.
- Any request for re-grading must be submitted to the Grader within one week of the completion of grading.

Tests:

- Tests are open-book, open-notes, calculators allowed.
- No makeup will be provided for any test missed.

Project:

- The project will consist of hardware construction and firmware development and it is expected that it will take approximately 60 hours to complete. Many steps of the project will be completed during the lab sessions.
- Projects teams will consist of 1 or 2 students.
- Interim deadlines for hardware construction and some software milestones will occur as part of the lab exercises..

Labs:

- You should complete and submit each week's lab assignment by the beginning of your lab session in the following week. There is no credit for work submitted after this time.

Course Schedule

- Architecture (3 hrs)
- Important C Language Details (e.g. C99 stdbool/stdint, volatile keyword) (0.5 hrs)
- Basic I/O cell design, application with port masking, bitbanding (4 hrs)
- Clocks (1 hr)
- Assembly/C mixed code, register and calling conventions (2.5 hrs)
- Pipeline operation and cycle-exact functions (1.5 hrs)
- UART theory and application (2 hrs)
- SPI bus theory and application to graphics LCD, graphics drivers (3 hrs)
- Exam 1 (1.5 hrs)
- Pulse-width modulation theory and application (1.5 hrs)
- Analog-to-digital theory (SAR) and application (2 hrs)
- Interfacing to signal conditioning and measurement techniques (3 hrs)
- Finite and infinite impulse response digital filtering, ring buffers (1.5 hrs)
- Keyboard interfacing (debouncing theory, open-drain outputs) (0.5 hr)
- Interrupt theory and application to a keyboard driver (1.5 hrs)
- Timer and CCP theory and application to frequency and time measurement (1.5 hrs)
- Project support lecture content (7 hrs)
- Exam 2 (1.5 hrs)

The instructor reserves the right to make changes in the schedule as needed as the class progresses.

The official dates for registration, census, and dropping are available at www.uta.edu/acadcal.

Academic Integrity

This information is copied from <http://www.uta.edu/conduct/academic-integrity/index.php>.

The University of Texas at Arlington strives to uphold and support standards of personal honesty and integrity for all students consistent with the goals of a community of scholars and students seeking knowledge and responsibility. Furthermore, it is the policy of the University to enforce these standards through fair and objective procedures governing instances of alleged dishonesty, cheating, and other academic/non-academic misconduct.

Scholastic dishonesty includes, but is not limited to, cheating, plagiarism, and collusion on an examination or an assignment being offered for credit. Each student is accountable for work submitted for credit, including group projects.

- Cheating
 - Copying another's test or assignment (added note: remember this includes homework!)
 - Communication with another during an exam or assignment (i.e. written, oral or otherwise)
 - Giving or seeking aid from another when not permitted by the instructor
 - Possessing or using unauthorized materials during the test
 - Buying, using, stealing, transporting, or soliciting a test, draft of a test, or answer key
- Plagiarism
 - Using someone else's work in your assignment without appropriate acknowledgement
 - Making slight variations in the language and then failing to give credit to the source

- Collusion
 - Without authorization, collaborating with another when preparing an assignment

Safety Rules for ERB 126 and ERB 127 Labs:

Scope:

- All UTA safety rules and regulations must be followed.
- These rules are in addition to UTA lab safety rules.
- In the event that a rule contained below is in conflict with UTA lab safety rules, the UTA safety rules shall supersede.

General rules:

- Students can only be in the lab when a graduate teaching assistant (GTA), faculty member, or staff member is present.
- Students should be professional at all times in the lab.
- Food and drinks are not allowed in the lab at any time.
- When leaving the bench, turn off all lab equipment and unplug soldering irons.
- When leaving the bench, make sure that all cables, tools, and soldering equipment are properly stored in the correct location.
- Please ensure that the lab is kept in a neat and tidy manner.
- Please pick up any loose wires or parts on the bench and floor before leaving the lab. There is a push broom and dust pan in both rooms.
- Note any hazards observed in the lab to the GTA, faculty, or staff member immediately.
- For test equipment incorporating multi-language menus, such as oscilloscopes, please return the language to English before leaving the bench.
- When returning parts that are not consumables, make certain that the parts are returned to the correct drawer. If you are not certain, please leave them with the GTA, faculty member, or staff member.
- Students should store backpacks and similar items in a way that does not create a trip hazard to others.

Personal protective equipment (PPE):

- Safety glasses must be worn at all times when using the soldering irons or using cutting tools, such as diagonal cutters, in the lab.

Soldering irons:

- Soldering irons should be used with care, while wearing safety glasses, and only after receiving training.
- When soldering and removing parts, or reworking a board, please use special care to ensure that solder is not splattered.
- Soldering irons must be placed back in their soldering station holder when not soldering to prevent the chance of injury or fire.
- Please keep the soldering station sponges wet when cleaning the iron tip but ensure that water is not spilled on the floor creating a slip hazard.
- Some solders can contain lead, so wash hands thoroughly after using the soldering irons.
- Soldering stations must be allowed to cool and then stored in the metal drawers below the bench.
- No self-contained butane soldering irons are permitted.

Hand tools:

- Hand tools must be used with care and only when safety glasses are being worn.
- Diagonal cutters in particular can create tension on the wires during the cutting process, ejecting the loose wire, so please use special care.
- Powered cutting tools (e.g., drills, "Dremels") should not be used in the ERB126 and ERB127 labs. Use the designated Makerspace areas that are designed to handle the additional safety requirements and dust inhalation hazards instead.

Electrical hazards:

- The labs for these classes use voltages of 30V or less, but care must always be shown in using electrical circuits, regardless of the voltage.
- Do not use voltages of more than 30V unless approved in writing by the instructor.
- Do not modify the wiring or attempt repair of any lab equipment.
- Most of the lab equipment operates from 120V AC, which is a lethal voltage. Never pull on a cord to unplug it as this can cause damage to the strain relief and insulation, resulting in exposed conductors.
- Please notify the GTA, faculty member, or staff member and stop using the equipment immediately if you see nicks or damage to a power cord.

Computers:

- Students should not install any software on the lab computers without approval of the GTA, faculty member, or staff member.
- Students should not remove any of the cables on the computer and the monitor on the bench.
- For external connection, an HDMI cable is provided at each workstation for configuring Raspberry Pi and similar computer hardware. This cable should not be disconnected from the monitor.

Institution Information

UTA students are encouraged to review the below institutional policies and informational sections and reach out to the specific office with any questions. To view this institutional information, please visit the [Institutional Information](http://www.uta.edu/provost/administrative-forms/course-syllabus/index.php) page

(<http://www.uta.edu/provost/administrative-forms/course-syllabus/index.php>) which includes the following policies among others:

- Drop Policy
- Disability Accommodations
- Title IX Policy
- Academic Integrity
- Student Feedback Survey
- Final Exam Schedule

Additional Information

Attendance:

At The University of Texas at Arlington, taking attendance is not required but attendance is a critical indicator of student success. Each faculty member is free to develop his or her own methods of evaluating students' academic performance, which includes establishing course-specific policies on attendance. As the instructor of this section, However, while UT Arlington does not require instructors to take attendance in their courses, the U.S. Department of Education requires that the University have a mechanism in place to mark when Federal Student Aid recipients "begin attendance in a course." UT Arlington instructors will report when students begin attendance in a course as part of the final grading process. Specifically, when assigning a student a grade of F, faculty report must the last date a student attended their class based on evidence such as a test, participation in a class project or presentation, or an engagement online via Canvas. This date is reported to the Department of Education for federal financial aid recipients.

Emergency Exit Procedures:

Should we experience an emergency event that requires evacuation of the building, students should exit the room and move toward the nearest exit. When exiting the building during an emergency, do not take an elevator but use the stairwells instead. Faculty members and instructional staff will assist students in selecting the safest route for evacuation and will make arrangements to assist individuals with disabilities.

Student Success Programs:

UT Arlington provides a variety of resources and programs designed to help students develop academic skills, deal with personal situations, and better understand concepts and information related to their courses. Resources include [tutoring by appointment](#), [drop-in tutoring](#), [etutoring](#), [supplemental instruction](#), [mentoring](#) (time management, study skills, etc.), [success coaching](#), [TRIO Student Support Services](#), and [student success workshops](#). For additional information, please email resources@uta.edu, or view the [Maverick Resources](#) website.

Emergency Phone Numbers

In case of an on-campus emergency, call the UT Arlington Police Department at **817-272-3003** (non-campus phone), **2-3003** (campus phone). You may also dial 911. Non-emergency number 817-272-3381