## EE 3310 -- Microprocessors Summer 1992 -- Section 601 MW 8:00 - 9:50 am

**Instructor:** J. H. Losh

Office: 142b EB-II Phone: (817) 794-5661 FAX #: (817) 273-2253

Hours: 10-11am MW, or by appt.

**Textbook:** Uffenbeck, John, <u>The 8086/8088 Family - Design, Programming, Interfacing, Prentice Hall, 1987. ISBN 0-13-246752-6.</u>

<u>and</u>

Course content will be based on Chapters 1-8 and 13 and the Appendices.

**References:** Uffenbeck, John, <u>Microcomputers and Microprocessors</u> -- <u>The</u> 8085, and Z80, Prentice Hall, 1985. ISBN 0-13-580309-8.

<u>8080,</u>

<u>iAPX 86/88, 186/188 User's Manual: Hardware Reference,</u> Intel Literature Department, 1985. ISBN 0-917017-36-6.

<u>iAPX</u> <u>86/88, 186/188</u> <u>User's Manual</u>: <u>Programmers Reference</u>, Intel Literature Department, 1985. ISBN 1-55512-010-5.

Liu, Yu-cheng, *et. al.*, <u>Microcomputer Systems</u>: <u>The 8086/8088 Family</u>: <u>Architecture</u>, <u>Programming</u>, <u>and Design</u>, Second ed., 1986. ISBN 0-13-580499-X.

Prentice Hall,

Coffron, James W., <u>Programming the 8086/8088</u>, Sybex Publishing, 1983. ISBN 0-89588-120-9.

Grade Structure: A (90-100), B (80-89), C (70-79), D (60-69), F(0-59)

	Method 1	Method 2
Quizzes	25%	20%
Test 1	25%	20%
Test 2	0%	20%
S/W Project	25%	20%
H/W Project	25%	20%
(Take the best of the two methods)		

- **Course** A) The last undergraduate day to drop with an automatic grade **Info:** of W is Tuesday, 30 June 1992.
  - B) The absolute last day to withdraw passing (WP) is Tuesday, 28 July 1992.
    - C) There are no make-ups for quizzes for any reason.
      One quiz grade will be dropped in calculating average.
    - D) No grade of incomplete (X) will be awarded for any reason.
    - E) All exams are open book/notes.
    - F) Any cheating will result in a grade of zero on the work

submitted and subsequent filing for disciplinary action with Associate Dean of Engineering.

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G) It is very important that you work the assigned homework. It will <u>not</u> be graded or submitted to me (there are too many solution manuals around), but the homework will be highly respective of the quiz questions.

**Grading:** Quizes and Test1: Any grievances with the assigned grades should be in writing and submitted within 1 week of the date of return to the grader.

<u>Test2</u>: The exams will be retained by me and will be available for examination on the first days of the Fall semester.

<u>Project</u>: Although the project may be split between two students, both are responsible for knowing the function of the entire project.

Submissions will result in a grade of zero for all parties involved.

Availability of Final Grades: Due to the Privacy Act of 1974, no grades, in coded or other form can be posted, without exception. I will mail grade information to you if you provide a SASE to me at the time of the final.

**Computer** The projects in this class will be based around the use of the PC **Use:** Lab in Room 255 of EB II. The primary packages used will be OrCAD SDT and VST and various languages (C, C++, Pascal, FORTRAN, MASM, and Turbo Assembler).

Because of theft from accounts in previous semesters, password changing is highly encouraged. If someone steals your code or file and submits it as theirs, your grade may suffer.

**Prereqs:** 1) Combinational Logic Design

- 2) Synchronous State Machine Design
- 3) Knowledge of Gates, Flip-flops, Decoders, Buffers, ...
- 4) Knowledge of a major programming language.

**Topics:** 1) Microprocessor Architechture Evolution

- 2) OpCodes, Instructions, and Program Execution
- 3) Assembly Language Programming
- 4) Hardware Overview
- 5) Microprocessor System Design