

EE 3310 -- Microprocessors
Summer 1992 -- Section 601
MW 8:00 - 9:50 am

Instructor: J. H. Losh

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Hours: 10-11am MW, or by appt.

Textbook: Uffenbeck, John, The 8086/8088 Family - Design, Programming, and Interfacing, Prentice Hall, 1987. ISBN 0-13-246752-6.

Course content will be based on Chapters 1-8 and 13 and the Appendices.

References: Uffenbeck, John, Microcomputers and Microprocessors -- The 8085, and Z80, Prentice Hall, 1985. ISBN 0-13-580309-8.

iAPX 86/88, 186/188 User's Manual: Hardware Reference, Intel Literature Department, 1985. ISBN 0-917017-36-6.

iAPX 86/88, 186/188 User's Manual: Programmers Reference, Intel Literature Department, 1985. ISBN 1-55512-010-5.

Liu, Yu-cheng, *et. al.*, Microcomputer Systems: The 8086/8088 Family: Architecture, Programming, and Design, Second ed., Prentice Hall, 1986. ISBN 0-13-580499-X.

Coffron, James W., Programming the 8086/8088, Sybex Publishing, 1983. ISBN 0-89588-120-9.

Grade Structure: A (90-100), B (80-89), C (70-79), D (60-69), F(0-59)

	Method 1	Method 2
Quizzes	25%	20%
Test 1	25%	20%
Test 2	0%	20%
S/W Project	25%	20%
H/W Project	25%	20%
(Take the best of the two methods)		

- Prereqs:**
- 1) Combinational Logic Design
 - 2) Synchronous State Machine Design
 - 3) Knowledge of Gates, Flip-flops, Decoders, Buffers, ...
 - 4) Knowledge of a major programming language.

- Topics:**
- 1) Microprocessor Architecture Evolution
 - 2) OpCodes, Instructions, and Program Execution
 - 3) Assembly Language Programming
 - 4) Hardware Overview
 - 5) Microprocessor System Design