EE 4342 Microprocessor System Design Project
Spring 2006
MW 5:30-6:50pm, 106 NH (Lecture)
M or W 7:00-9:50pm, 148 NH (Lab)

Instructor:
Jason Losh, Ph.D.
jlosh@uta.edu
Office Hours will begin at 6:50pm MW in 148 NH.
E-mail is the quickest method of contacting me on non-class days.

Textbook:
Extensive references, datasheets, application notes, and class notes will be provided on the course web site at http://omega.uta.edu/~jlosh/.

Listserv:
Please sign up for the EE4342-L listserv to receive the latest updates (go to http://listserv.uta.edu for details)

Modified Catalog Course Description:
Design principles for digital and analog instrumentation utilizing open computer architectures (ISA) and proprietary microcontrollers. The course is intended to provide a comprehensive design experience in microprocessor- and microcontroller-based applications. Will include bus protocol analysis, timing design, simulation, prototype development, and physical debugging of digital circuits. Prerequisite: EE 3310, EE 3317, EE 3340, and senior standing.

Comments on the Course:
All topics discussed in class will be accompanied with demonstrations in class using real hardware and software (C and assembly based). The course will work with the 80x86 microprocessors and PIC microcontrollers.

Prerequisite Background:
Familiarity with the 8086 processor or a very good knowledge of other processors is essential. Essential background also includes assembly language programming, memory organization, mapping, and timing, basic i/o interfacing, and interrupt operation. Also, knowledge of C or C++ is important.

A good understanding of BJT and FET circuits and Fourier series and transforms will also be very useful.

Course Topics:
- Course introduction and discussion of course objectives
- Presentation of suggested topics
- Microprocessors and microcontrollers supported in lab: Intel, Microchip, and Motorola
- Platform demonstrations (ISA bus PC and PIC designs)
- Comparison of microprocessor and microcontroller architectures
- Review of 8086 assembly language programming and high-level language function calls
- Introduction to PIC assembly language and high-level programming
- Review of 8086 maximum-mode architecture, interfacing, timing, and memory
- Memory: organization and interfacing, refresh, ROM (EPROM and flash) and RAM
- Interrupts: software and hardware interrupts, interrupt service routines, and interrupt hooking
- Interrupts v. polling: latency, effective rates, and efficiency
- Project specific i/o issues: digital and analog signal interfacing, isolation, ground loops, noise, and EMI
- Project specific i/o buses and interfaces: PCI, ISA, RS-232/422/423/485, IEEE-1284, and USB

Additional References:
Many resources will be provided in addition to the textbook. These documents will be posted at http://omega.uta.edu/~jlosh/.

Digital resources:
- *Digital Logic Design Principles* by Balabanian and Carlson, 2001, ISBN 0-471-29351-2
- *An Engineering Approach to Digital Design* by Fletcher, 1980, ISBN 0-13-277699-5

80x86 software resources:
- *The Art of Assembly Language Programming* by Hyde, http://cs.smith.edu/~thiebaut/ArtOfAssembly/artofasm.html
- *Intel Architecture Software Developer's Manual, vols. 1 through 3*, Intel 1999
- *The Programmer's PC Sourcebook* by Hogan, 1988, ISBN 1-55615-118-7

80x86 hardware resources:
- *Intel 386™ DX Microprocessor Datasheet*, Intel, 1995
- *Intel Pentium® Processor with MMX Technology*, Intel, 1997
- *Intel Pentium 4® Processor with 512-KB L2 Cache*, Intel, 2002
- *Microprocessors vols. 1 and 2*, Intel, 1992, ISBN 1-55512-150-0
- *Microprocessors and Interfacing – Programming and Hardware*, 2nd ed. by Hall, 1992, ISBN 0-07-025742-6
- *The 8088 and 8086 Microprocessors – Programming, Interfacing, Software, Hardware, and Applications, 3rd ed.* by Triebel and Singh, ISBN 0-13-010560-0
- *Microprocessor Systems: The 8086/8088 Family*, 2nd ed. by Liu and Gibson, 1986, ISBN 0-13-580499-X

Microcontroller resources:
- *PIC 16C745/765 Microcontrollers with USB Datasheet*, Microchip, 2000
- *PIC 18F452 Microcontroller Datasheet*, Microchip, 2002
- *PICDEM User's Guide*, Microchip, 2001

Important Dates:
First Class (Wednesday, 1/18), Project Plan Due Date (Monday, 1/30), Census Date (Wednesday, 2/1), First UG Drop Date (Friday, 2/24), Midsemester (Friday, 3/10), Spring Vacation (Monday-Friday, 3/13-17), Test 1 (Monday, 3/20), Last Drop Date (Friday, 4/14), Test 2 (Monday, 4/24~~ or Wednesday, 4/26~~), Project Due Date (Wednesday, 5/3)

Performance Assessment:
- Grade scale: A (90-100), B (75-89), C (60-74), D (50-59), and F (0-49)
- Grade calculation: (Test1 + Test2 + Project × 2) / 4
- The instructor reserves the right to make reasonable changes in performance evaluation as needed.

Shared Graduate Teaching Assistants:
Asma Al-Tamimi, altamimi@arri.uta.edu, MW 5:30-7pm (EE6314)
Saurabh Bhatt, sbhatt@exchange.uta.edu, MW 7-10pm, T 5:30-10pm (EE4342 lab)
Qi Dong, qi.dong@uta.edu, Th 5:30-10pm, F 5:30-8pm (EE4342 class)

Lab Hours: M-Th 5:30-10pm, F 5:30-8pm
If the lab is empty as of 8:40pm (allowing ample time for students attending a 7-8:20pm class to reach the lab), the GTAs may leave the lab.

The lab may also be open for open lab hours M-F 9am-5:30pm subject to available staff.

Lab Orientation:
EE department policy requires that students utilizing 148NH attend a safety orientation session.

Cost:
No textbook will be required. The project cost is expected to be between $100 and $250 per team.

Tests (50% of Grade):
- Calculators, rulers, pencils, pens, books, and notes will be allowed during tests.
- Any device capable of compiling or emulating any 80x86 code or 16F877A code can not be used during in-class portions of the tests.
- Take home portions of tests will be due within one week of assignment.
- No makeup will be provided for any test.
- Any request for re-grading must be submitted to the grader within one week of the return date.

Project (50% of Grade):
- Project teams will consist of one to three team members.
- The initial project plan is due on Monday, 1/30.
- Interim deadlines will vary upon the project chosen.
- The final project will be presented in class on Wednesday, 5/3.

Academic Honesty:
It is the philosophy of The University of Texas at Arlington that academic dishonesty is a completely unacceptable mode of conduct and will not be tolerated in any form. All persons involved in academic dishonesty will be disciplined in accordance with University regulations and procedures. Discipline may include suspension or expulsion from the University. “Scholastic dishonesty includes but is not limited to cheating, plagiarism, collusion, the submission for credit of any work or materials that are attributable in whole or in part to another person, taking an examination for another person, any act designed to give unfair advantage to a student or the attempt to commit such acts.” (Regents’ Rules and Regulations, Part One, Chapter VI, Section 3, Subsection 3.2, Subdivision 3.22). ANY CHEATING WILL RESULT IN SEVERE PENALTIES. All work submitted must be original. If derived from another source, a full bibliographical citation must be given.

EE Department Policy requires that you sign and return a letter acknowledging the College of Engineering Ethics policy.

Americans with Disabilities Act:
If you require an accommodation based on disability, please feel free to meet with me during the first week of the semester to make sure that you are properly accommodated. Contact Dr. Cheryl Cardell (272-3670) or Mr. Jim Hayes (272-3364) for more information.