

EE 5314-001 Embedded Microcontroller System Design

Fall 2016

5:30-6:50pm MW, NH105 (Lecture)

7:00-9:50pm MTWThF, NH148 or NH148A (Lab)

Instructor:

Jason Losh, Ph.D.

jlosh@uta.edu

Office Hours are after 6:50pm MW outside NH105.

E-mail is the quickest method of contacting me on non-class days.

No phone or office has been assigned by the University.

Textbook:

No textbook will be required for this course. Extensive references, datasheets, application notes, and class notes will be provided on the course web site at <http://omega.uta.edu/~jlosh/>.

Listserv:

Please sign up for the EE5314-L listserv to receive the latest updates (goto <http://listserv.uta.edu> to manage your subscriptions or send a message to listserv@listserv.uta.edu with no subject line and the command SUBSCRIBE EE5314-L as the message body).

Catalog Description:

5314. EMBEDDED MICROCONTROLLER SYSTEMS (3-0). Hardware/software development techniques for microcontroller systems with emphasis on hardware-software interactions, programming internal peripherals, and real-time control and conditioning of external devices. Topics include: code efficiency issues, pin reuse issues, interrupt-driven processing, USART operations, I2C and SPI bus peripherals, and use of internal peripherals.

Prerequisites:

Very good knowledge of at least one microprocessor or microcontroller is mandatory. Essential background includes assembly language programming, memory organization, memory mapping, bus timing, basic i/o interfacing, and interrupt operation.

A good understanding of BJT and FET circuits, Fourier series and transforms, 1st and 2nd order control loops, and basic communications will also be very useful.

Prerequisite Exam:

A prerequisite exam worth 10% of the class grade will be given on the second day of class to verify that the student has knowledge of basic C-language programming skills. We will spend a few minutes after the quiz to allow you to determine if you should stay in the course or consider taking another class, as the last day of Late Registration is the day of the exam.

Measurable Student Learning Outcomes:

Knowledge of:

- Differences between Harvard and von Neuman architectures
- Differences of microcontroller and microprocessor architectures
- Comparison of standard microprocessor and digital signal processor memory bus and ALU architectures
- Study of microcontroller peripherals including timers, PWM, interrupts, GPIO ports, and ADCs
- Determining microcontroller memory, speed, and capabilities to solve a task
- Interfacing with SPI, serial, and CAN buses
- Measurement and instrumentation applications
- Digital filtering applications
- Device control: Motors, servos, relays, heavy AC and DC loads
- Real-time control applications

Important Dates:

First Class (Monday, 8/29), Prerequisite Exam (Wednesday, 8/31 @ 5:30pm), Late Registration Ends (Wednesday, 8/31), No Class (Monday, 9/5), Census Date (Monday, 9/12), Test 1 (Wednesday, 10/19), Last Drop Date (Wednesday, 11/2), Test 2 (Wednesday, 11/30), and Project Defense (Wednesday, 12/7 @ 5:30pm)

(The official dates for registration, census, and dropping are available at www.uta.edu/acadcal and supersede these dates if a conflict exists)

Performance Assessment:

- Grade scale: A (90-100), B (80-89), C (70-79), D (60-69), and F (0-59)
- Grade calculation: $(\text{Prerequisite Exam} \times 0.1) + (\text{Attendance} \times 0.1) + (\text{Test 1} \times 0.25) + (\text{Test 2} \times 0.25) + (\text{Project} \times 0.3)$
- The instructor reserves the right to make reasonable changes in performance evaluation as needed.

Shared Graduate Teaching Assistants:

All office hours are in the lab (NH148).

Hours and GTAs will be announced in class.

Tests:

- Calculators, rulers, pencils, pens, books, and notes will be allowed during tests.
- Any device capable of compiling or emulating microcontrollers or microprocessors can not be used during in-class portions of the tests.
- No makeup will be provided for any test.
- Any request for re-grading must be submitted to the grader within one week of the first return date.

Cost:

No textbook will be required. Parts required for the project will generally be less than \$25 per team.

Projects:

- The project will consist of hardware construction and firmware development and it is expected that it will take approximately 100 hours to complete.
- Projects will be individual student assignments.
- Projects will be defended on the last class day during class hours with interim deadlines for hardware construction and some software milestones.
- A lab in 148NH is available to construct the hardware portion of your project and test your project, provided you attend a mandatory lab safety orientation.

Attendance Policy:

Attendance will be taken on 4 class days at random, with the provision for one absence with penalty. A grade of zero will be recorded for any exam, test, or project deadline that is missed. The student is responsible for obtaining notes on any material missed.

Academic Integrity:

It is the philosophy of The University of Texas at Arlington that academic dishonesty is a completely unacceptable mode of conduct and will not be tolerated in any form. All persons involved in academic dishonesty will be disciplined in accordance with University regulations and procedures. Discipline may include suspension or expulsion from the University. According to the UT System Regents' Rule 50101, §2.2, "Scholastic dishonesty includes but is not limited to cheating, plagiarism, collusion, the submission for credit of any work or materials that are attributable in whole or in part to another person, taking an examination for another person, any act designed to give unfair advantage to a student or the attempt to commit such acts."

EE Department Policy requires that you sign and return a letter acknowledging the College of Engineering Ethics policy.

Drop Policy:

Students may drop or swap (adding and dropping a class concurrently) classes through self-service in MyMav from the beginning of the registration period through the late registration period. After the late registration period, students must see their academic advisor to drop a class or withdraw. Undeclared students must see an advisor in the University Advising Center. Drops can continue through a point two-thirds of the way through the term or session. It is the student's responsibility to officially withdraw if they do not plan to attend after registering. Students will not be automatically dropped for non-attendance. Repayment of certain types of financial aid administered through the University may be required as the result of dropping classes or withdrawing. Contact the Financial Aid Office for more information.

Americans with Disabilities Act:

The University of Texas at Arlington is on record as being committed to both the spirit and letter of all federal equal opportunity legislation, including the *Americans with Disabilities Act (ADA)*. All instructors at UT Arlington are required by law to provide "reasonable accommodations" to students with disabilities, so as not to discriminate on the basis of that disability. Any student requiring an accommodation for this course must provide the instructor with official documentation in the form of a letter certified by the staff in the Office for Students with Disabilities, University Hall 102. Only those students who have officially documented a need for an accommodation will have their request honored. Information regarding diagnostic criteria and policies for obtaining disability-based academic accommodations can be found at www.uta.edu/disability or by calling the Office for Students with Disabilities at (817) 272-3364.

Student Support Services:

The University of Texas at Arlington provides a variety of resources and programs designed to help students develop academic skills, deal with personal situations, and better understand concepts and information related to their courses. These resources include tutoring, major-based learning centers, developmental education, advising and mentoring, personal counseling, and federally funded programs. For individualized referrals to resources for any reason, students may contact the Maverick Resource Hotline at 817-272-6107 or visit www.uta.edu/resources for more information.

Electronic Communication Policy:

The University of Texas at Arlington has adopted the University "MavMail" address as the sole official means of communication with students. MavMail is used to remind students of important deadlines, advertise events and activities, and permit the University to conduct official transactions exclusively by electronic means. For example, important information concerning registration, financial aid, payment of bills, and graduation are now sent to students through the MavMail system. All students are assigned a MavMail account. Students are responsible for checking their MavMail regularly. Information about activating and using MavMail is available at <http://www.uta.edu/oit/email/>. There is no additional charge to students for using this account, and it remains active even after they graduate from UT Arlington.

To obtain your NetID or for logon assistance, visit <https://webapps.uta.edu/oit/selfservice/>. If you are unable to resolve your issue from the Self-Service website, contact the Helpdesk at helpdesk@uta.edu.

Lab Safety Training:

EE department policy requires that students utilizing NH148 attend a safety orientation session.