

**EE 6313 Advanced Microprocessor Systems**  
**Fall 2009**  
**4-5:20pm MW, 109NH**

**Instructor:**

Jason Losh, Ph.D.

[jlosh@omega.uta.edu](mailto:jlosh@omega.uta.edu)

Office Hours are after 5:20pm MW outside 208WH or in 148NH.

E-mail is the quickest method of contacting me on non-class days.

**Textbook:**

*Computer Organization and Architecture: Designing for Performance*, 7<sup>th</sup> ed., William Stallings, Prentice Hall 2006, ISBN 0-13-185644-8.

Extensive references, datasheets, application notes, and class notes will be provided on the course web site at <http://omega.uta.edu/~jlosh/>.

**Listserv:**

Please sign up for the EE6313-L listserv to receive the latest updates (go to <http://listserv.uta.edu> for details)

**Catalog Course Description:**

**6313. ADVANCED MICROPROCESSOR SYSTEMS (3-0).** Study of the advanced microprocessor architectures including 32/64-bit RISC processors from leading manufacturers. The design concepts, performance and architectural limitations of RISC and CISC families of microprocessors will be compared based on detailed architectural analysis of the selected devices. Topics include: address/instruction pipelines, burst cycles, memory caching and cache coherency issues, register renaming, speculative instruction execution and other performance-oriented techniques. Prerequisite: EE 5313 or consent of instructor.

**Course Topics:**

- CPU Design: ALU, Registers, Control Unit
- Instruction Sets: Minimalism, Orthogonality, RISC/CISC
- Predication, Speculative Execution, and Out of Order Execution
- Protected Mode Operation
- Multitasking Operating Systems
- Pipelining: Design, Hazards, Stall/Forwarding Resolution
- Parallelism and Superscalar Architectures
- Cache Memory Design: Mapping, Miss/Hit, Replacement, Write-back Strategies
- Virtual Memory: Paging, Segmentation, Translation and Look-around
- High-speed I/O Design, Modern Protocols: PCIe, SATA

**Important Dates:**

First Class (Monday, 8/24), No Class (Monday, 9/7), Census Date (Wednesday, 9/9), Project 1 Defense (Wednesday, 10/21 at 4pm), Last Drop Date (Friday, 10/30), and Project 2 Defense (Wednesday, 12/2 @ 4pm)

**Performance Assessment:**

- Grade scale: A (90-100), B (80-89), C (70-79), D (60-69), and F (0-59)
- Standard grade calculation: (Project 1 + Project 2) / 2
- The instructor reserves the right to make reasonable changes in performance evaluation as needed.

**Graduate Teaching Assistants:**

All office hours are in the lab (148 or 148A NH).

EE5314/4328 GTA: Ankit Doshi, [ankitgdoshi+5314@gmail.com](mailto:ankitgdoshi+5314@gmail.com)

EE6313 GTA: Rajiv Loonia, [rajivloonias@gmail.com](mailto:rajivloonias@gmail.com)

**Monday 5.5 hrs**

11:00 am - 2:30 pm    3.5 hrs    Rajiv

4:00 pm - 6:00 pm    2 hrs    Rajiv

**Tuesday 6.5 hrs**

12:00 noon - 3:30 pm    3.5 hrs    Ankit

6:30 pm - 9:30 pm    3 hrs    Ankit

**Wednesday 8 hrs**

12:00 noon - 2:30 pm    2.5 hrs    Rajiv

4:00 pm - 9:30 pm    5.5 hrs    Rajiv

**Thursday 7.5 hrs**

11:00 am - 3:30 pm    4.5 hrs    Ankit

6:30 pm - 9:30 pm    3 hrs    Ankit

**Friday 10.5 hrs**

11:00 am - 4:00 pm    5 hrs    Ankit

4:00 pm - 9:30 pm    5.5 hrs    Rajiv

**Projects:**

- Project teams will consist of one or two team members. Teams of three or more members will not be permitted, without exception.
- 2 projects will be assigned.

**Academic Integrity:**

It is the philosophy of The University of Texas at Arlington that academic dishonesty is a completely unacceptable mode of conduct and will not be tolerated in any form. All persons involved in academic dishonesty will be disciplined in accordance with University regulations and procedures. Discipline may include suspension or expulsion from the University.

"Scholastic dishonesty includes but is not limited to cheating, plagiarism, collusion, the submission for credit of any work or materials that are attributable in whole or in part to another person, taking an examination for another person, any act designed to give unfair advantage to a student or the attempt to commit such acts." (Regents' Rules and Regulations, Series 50101, Section 2.2)

EE Department Policy requires that you sign and return a letter acknowledging the College of Engineering Ethics policy.

**Americans with Disabilities Act:**

The University of Texas at Arlington is on record as being committed to both the spirit and letter of federal equal opportunity legislation; reference Public Law 92-112 - The Rehabilitation Act of 1973 as amended. With the passage of federal legislation entitled Americans with Disabilities Act (ADA), pursuant to section 504 of the Rehabilitation Act, there is renewed focus on providing this population with the same opportunities enjoyed by all citizens.

As a faculty member, I am required by law to provide "reasonable accommodations" to students with disabilities, so as not to discriminate on the basis of that disability. Student responsibility primarily rests with informing faculty of their need for accommodation and in providing authorized documentation through designated administrative channels. Information regarding specific diagnostic criteria and policies for

obtaining academic accommodations can be found at [www.uta.edu/disability](http://www.uta.edu/disability). Also, you may visit the Office for Students with Disabilities in room 102 of University Hall or call them at (817) 272-3364.

**Student Support Services:**

The University of Texas at Arlington supports a variety of student success programs to help you connect with the University and achieve academic success. These programs include learning assistance, developmental education, advising and mentoring, admission and transition, and federally funded programs. Students requiring assistance academically, personally, or socially should contact the Office of Student Success Programs at 817-272-6107 for more information and appropriate referrals.